

09/849,707

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,775,112 B1
DATED : November 12, 2004
INVENTOR(S) : Gregory J. Smith et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [57], **ABSTRACT**, "and cause the active device to 'turn on'" should read -- and cause the active device to "turn on." --

Column 7,

Line 20, "SO as to" should read -- so as to --

Column 8,

Line 6, "1.8. k Ω ." should read -- 1.8 k Ω . --

Line 23, "a GaAs, FET to JFET" should read -- a GaAs, FET JFET --

Column 10,

Line 31, "inverter 160" should read -- inverter I₆₀ --

Line 38, "at node N₆" should read -- at node N₆₁ --

Column 11,

Line 59, "ground terminal response to" should read -- ground terminal in response to --

Line 66, "the protection circuit fry" should read -- the protection circuit further --

Column 12,

Line 2, "the signal firm the power" should read -- the signal from the power --

Line 15, "slower than the fist" should read -- slower than the first --

Column 13,

Line 6, "transient evens" should read -- transient events --.

Line 56, "a slow ESD event an the power" should read -- a slow ESD event on the power --

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Column 14.

Line 48, "to couple a fast signal" should read -- to couple a fast transient signal --

Signed and Sealed this

Fifteenth Day of February, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office